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Docket No.: 60188-031

**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277  
Kentaro SHIOMI , et al. : Confirmation Number: 6677  
Serial No.: 09/779,440 : Group Art Unit: 2132  
Filed: February 09, 2001 : Examiner:  
For: LSI DESIGN METHOD AND VERIFICATION METHOD

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop IDS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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MAY 04 2004

Technology Center 2100

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each non-English language reference was first cited in a corresponding foreign application search report or office action and its relevance discussed therein. A copy of the

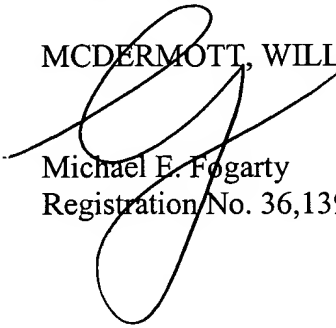
09/779,440

foreign search report or office action, together with an English language version thereof, is attached for the Examiner's information.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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**Date: May 3, 2004**



SHEET 1 OF 1

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  (PTO-1449)	ATTY. DOCKET NO. <b>60188-031</b>	SERIAL NO. <b>09/779,440</b>
	APPLICANT <b>Kentaro SHIOMI , et al.</b>	
	FILING DATE <b>February 09, 2001</b>	GROUP <b>2132</b>

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sub>2</sub> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US			
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## FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number 4 -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		JP 10-198726 A	07/31/1998	VLSI TECHNOL INC		JAPAN (w/English Abstract)	

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		CURTIS, Kevin, " No Spread of IP core with no intellectual property protection." NIKKEI Electronics, Nikkei BP, No. 717, pp. 169-173, June 1, 1998.
		YOSHIMORI et al., " VSI Alliance and Technologies for IP Core Application." Toshiba Review, Toshiba Corporation, pp.27-30, Vol. 52, No. 12, December 1, 1997 with partial English Translation thereof.
		CURTIS, Kevin. " Core Proliferation: Security and Protection Issues with IP Integration." Electronic Design, pp. 108-112, January 12, 1998

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.